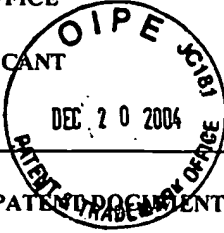


FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO.: 57941.000062		SERIAL NO.: 10/716,595	
LIST OF MATERIALS CITED BY APPLICANT					INVENTOR'S NAME: Michael FARMWALD et al.		EXAMINER: Tan Nguyen	
(Use several sheets if necessary)					FILING DATE: November 20, 2003		GROUP: 282 2827	



U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
TNT	1.	4360870	11/23/1982	McVey	710	9
	2.	4379222	04/05/1983	Hayter et al.	377	81
	3.	4394753	07/19/1983	Penzel	365	236
	4.	4494021	01/15/1985	Bell et al.	327	262
	5.	4513372	04/23/1985	Ziegler et al.	711	5
	6.	4520465	05/28/1985	Sood	365	203
	7.	4562435	12/31/1985	McDonough et al.	345	545
	8.	4597019	06/24/1986	Nishimoto et al.	386	27
	9.	4628489	12/09/1986	Ong	365	230.04
	10.	4637018	01/13/1987	Flora	714	700
	11.	4688197	08/18/1987	Novak et al.	365	189.12
	12.	4754433	06/28/1988	Chin et al.	365	189.02
	13.	4755937	07/05/1988	Glier	711	149
	14.	4785428	11/15/1988	Bajwa	365	233
	15.	4789960	12/06/1988	Willis	345	534
	16.	4811364	03/07/1989	Sager et al.	375	354
	17.	4849937	07/18/1989	Yoshimoto	365	189.05
	18.	4941128	07/10/1990	Wada	365	203
	19.	5001672	03/19/1991	Ebbers	365	230.05
	20.	5006982	04/09/1991	Ebersole et al.	710	263
	21.	5134699	07/28/1992	Aria et al.	710	35
TNT	22.	5173617	12/22/1992	Alsup et al.	327	158

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS AND SUBCLASS	TRANSLATION PROVIDED	
				Yes	No
TNT 23. ✓	02197553 ✓	05/18/1988	GB		
24. ✓	63217452 ✓	09/09/1988	JP		
25. ✓	63239676 ✓	10/05/1988	JP		
TNT 26. ✓	6443894 ✓	02/16/1989	JP		

EXAMINER TAN T. NGUYEN	DATE CONSIDERED 03/26/05
---------------------------	-----------------------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

12/20/04

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.: 57941.000062	SERIAL NO.: 10/716,595
LIST OF MATERIALS CITED BY APPLICANT (Use several sheets if necessary)				INVENTOR'S NAME: Michael FARMWALD et al.	EXAMINER: Tan Nguyen
				FILING DATE: November 20, 2003	GROUP: 2821 2827
OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)					
7-1	27. ✓	ALNES, Norsk Data Report, 11/10/1988, SCI: A Proposal For ✓			
	28. ✓	Gustavson, David B., 11/28/1988, SCI-C [1 and SCI 2], Scalable Coherent Interface ✓			
	29. ✓	Gustavson, David B. et al, 08/22/1988, SCIA [I and SCI 2], The Scalable Coherent Interface Project (Superbus), Draft ✓			
	30. ✓	Johnson, Mark G; Hudson, Edwin L., IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, 10/00/1988, Johnson (IEEE 10/88), A Variable Delay Line PLL For CPU- Coprocessor Synchronization ✓			
	31. ✓	Intel, Intel Publication Number 171873-001 Rev. A, 1981, iAPX432 GDP Datasheet, Intel iAPX43202 VLSI General Data Processor Data Sheet ✓			
	32. ✓	Intel, Intel Publication Number 17 1874-001 Rev. A, 1981, iAPX432 IP Datasheet, Intel iAPX43203 VLSI Interface Processor Data Sheet ✓			
	33. ✓	Numata, K. et. al., IEEE Journal of Solid State Circuits, vol. 24 No. 4, pp. 900-904, 08/00/1989, New Nibbled -Page Architecture for High Density DRAM's ✓			
	34. ✓	Peigrom, M. et al., ESSCIRC Dig. Tech. Papers, pp. 38-40, 09/00/1986, A 32Kbit Variable Length Shift Register for Digital Audio Application ✓			
	35. ✓	Poon, T. C., et al., IEEE Journal of Solid State Circuits, Vol. SC-22, No. 3, pp. 491-494, 1987, A CMOS DRAM-Controller Chip Implementation ✓			
	36. ✓	Watanabe, S. et. al., IEEE Journal of Solid State Circuits, vol. 24 No. 3, p. 763, 06/00/1982, An Experimental 16-Mbit CMOS DRAM Chip with a 100-MHz Serial Read/Write Mode ✓			
	37. ✓	Schanke, Morten, 05/05/1989, SCIB [1 and SCI 2], Proposal for Clock Distribution in SCI ✓			
	38. ✓	JEDEC, JC 42.3 Committee On MOS Memories, 09/14/1988, Minutes Of Meeting No. 47 JC-42.3 Committee On MOS Memories w/Attachment L (Proposal) ✓			
	39. ✓	JEDEC, JC 42.3 Committee On Mos Memories, 12/07/1988 Minutes Of Meeting No. 48 JC 42.3 Committee On MOS Memories WI Attachment L Proposal ✓			
	40. ✓	JEDEC, JC 42.3 Committee On 09/12/1989, Minutes of Meeting No. 51 JC 42.3 MOS Memories Committee On MOS Memories ✓			
	41. ✓	JEDEC, JC 42.3 Committee On MOS Memories, 12/06/1989, Minutes Of Meeting No. 52 JC 42.3 Committee On MOS Memories w/Attachment K ✓			
	42. ✓	Emmerson et al., IEEE MICRO, 12/00/1984, Fault Tolerance Achieved in VLSI ✓			
	43. ✓	Gelsinger et al., IEEE Spectrum October 1989, Microprocessors circa 2000, 1989 (pages 43-47) ✓			
	44. ✓	Patterson, David A. et al., Morgan Kaufmair Publishers, Inc., San Francisco, CA 1996, Computer Architecture A Quantitative Approach (Second Edition), excerpts ✓			
	45. ✓	Sawada, Kazuhiro et al., IEEE 1988 Custom Integrated Circuits Conference 1988, A 72K CMOS Channelless Gate Array With Embedded 1 Mbit Dynamic RAM ✓			
	46. ✓	Sutherland, Ivan E., Communications of the ACM, June 1989, Volume 32, Number 6, Micropipelines ✓			
	47. ✓	Lidington, Gary P., Digital Technical Journal, No. 7, August 1988, pp. 79-86, Overview of the MicroVAX 3 500/3600 Processor Module ✓			
7-1	48. ✓	Lidington, Gary P., Digital Technical Journal, No. 7, August 1988, pp. 79-86, Overview of the MicroVAX 3500/3600 Processor Module ✓			
EXAMINER		TAN T. NGUYEN		DATE CONSIDERED 03/21/05	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

SERIAL NO.:
10/716,595

EXAMINER:
Tan Nguyen

GROUP:
~~282~~ 2827

(Use several sheets if necessary)

OTHER MATERIALS (Including Author, Title, Date, Pertinent Pages, Etc.)

- | | | |
|----|---|--|
| 1. | ✓ | Copy of Hynix's Amended Supplemental Revised Preliminary Invalidity Contentions |
| 2. | ✓ | PATTERSON et al., Computer Architecture A Quantitative Approach |
| 3. | ✓ | JOHNSON et al., Session XI: High Speed Logic; THAM 11.3: A Variable Delay Line Phase Loop for CPU-Coprocessor Synchronization, ISSCC 88, Thursday, February 18, 1988, Continental Ballroom 6, 10:00 AM, pp. 142-143, 334 and 336 |
| 4. | — | JEONG et al., Design of PLL-Based Clock-Generation Circuits, IEEE Journal of Solid-State Circuits, Vol: SC-22, No. 2, April 1987, pp. 255-261 — |
| 5. | ✓ | GigaBit Logic, GaAs IC Data Book & Designer's Guide, May 1988, pp. 1.1-10.3 |
| 6. | ✓ | Intel, Iapx 432 Interconnect Architecture Reference Manual, 1982 |

DATE CONSIDERED 03/21/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number 10/716,595

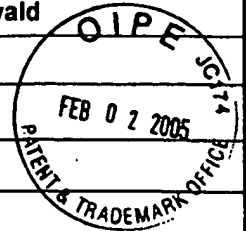
Filing Date November 20, 2003

First Named Inventor Michael Farmwald

Art Unit ~~2827~~ 2827

Examiner Name Tan Nguyen

Attorney Docket Number 57941.000062



Sheet

1

of

2

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TNT		US- 4744062	05-10-1988	Nakamura et al.	365 228
I		US- 4792926	12-20-1988	Roberts	365 189.02
TNT		US- 5687183	11-11-1997	Chesley	714 774
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initial	Cite No.	FOREIGN PATENT DOCUMENT		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	TRANSLATION	
		Country Code:	Number - Kind Code (if known)				YES	NO
TNT	✓	JP	62-135949	06-18-1987	Robaatsu		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	✓	JP	61-245255	10-31-1986	Terumi et al.		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	✓	JP	1-163849	06-28-1989	Takaaki		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	✓	JP	61-160129	07-19-1986	Akira		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	✓	JP	61-160130	07-19-1986	Akira		<input checked="" type="checkbox"/>	<input type="checkbox"/>
TNT	✓	JP	6-103153	04-15-1994	Chiesurei		<input checked="" type="checkbox"/>	<input type="checkbox"/>
							<input type="checkbox"/>	<input type="checkbox"/>

EXAMINER SIGNATURE TAN T. NGUYEN

DATE CONSIDERED 03/21/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

02/22/05

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number		10/716,595	
				Filing Date		November 20, 2003	
				First Named Inventor		Michael Farnwald	
				Art Unit		2827 2827	
				Examiner Name		Tan Nguyen	
Sheet	2	of	2	Attorney Docket Number		57941.000062	

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
TNT	/	JPO, Notice Of The Reason For Refusal, Patent Application No. 500879/95, dated 9/21/04, pgs. 1-4.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
TNT	/	Expert Report of David L. Taylor Regarding Invalidity, dated 1/28/05, pgs. 1-79.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>
			<input type="checkbox"/>	<input type="checkbox"/>

EXAMINER SIGNATURE	TAN T. NGUYEN	DATE CONSIDERED	03/21/05
--------------------	---------------	-----------------	----------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.